

FORM PTO-1449	Express Mail Number EV9447124415	cket Number (Optional): 100-16810 (P051-B-C1)	Application No. 10/032,930
INFORMATION DISCLOSURE CITATION IN AN APPLICATION		Applicant(s) Heikyung Min et al.	
<i>(Use several sheets if necessary)</i>		Filing Date October 29, 2001	Group Art Unit 2818

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Stanley Wolf Ph.D., Silicon Processing For the VLSI Era, Volume 2: Process Integration, Lattice Press, (1990), pp. 191 and 124-126.

EXAMINER

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DATE CONSIDERED

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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 5609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.